The ST interface is not working properly



Overview

Connection problems with these interfaces can prevent successful communication with the microcontroller, making it difficult to debug or flash new firmware.

Article Content

How to solve connection errors when connecting and ...

This article describes tips to prevent or fix errors encountered when connecting the STM32 target board to your computer such as: “No target connected”, “Target not found”, “No ST

STM32F411CEU6 Identifying and Fixing JTAG-SWD Connection

Connection problems with these interfaces can prevent successful communication with the microcontroller, making it difficult to debug or flash new firmware. This guide will help you identify the

Debugging STM32F030C8T6 How to Resolve JTAG-SWD

Make sure that the debugger type (e.g., ST-Link or J-Link) is correctly set, and that the correct communication protocol (SWD or JTAG) is selected. In STM32CubeIDE, go to the Debug

STM32F103RET6, ST-LINK V2,

For future travellers, I came across the DEV_TARGET_CMD_ERR error message when I was trying to connect to the nucleo dev board but using it

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ST-Link utilities don''t work anymore. : r/embedded

I tried rechecking the udev rules or using CubeIDE, but nothing works. Also, When I plug in the board, the ST-LINK LED turns red and blue couple of times and then remains red.

STM32 & ST-LINK

STM32CubeMx doesn''t configure the debug port by default, consequently ST-Link will stop working once you flash your code. You have to

Guide: Connecting your debugger

There are two commonly used connectors which expose only the SWD (Serial Wire Debug) interface or the full JTAG interface. If you are using one of ST''s official

How to fix communication issues between ST-LINK and

Discover effective solutions for firmware developers to resolve communication problems between ST-LINK and STM32 devices in this

Troubleshooting: ST-LinkV2 fails to connect to STM32F030K6T6

Hi, would really appreciate some ideas on how to troubleshoot ST-Link to STM32F030K6T6 connectivity, details below. I''ve designed a following PCB that is intended to drive

STLink V3-mini can''t program or debug anymore anymore

I have to say it''s quite annoying to have something that''s supposed to make your programming life easier, be so badly documented/tested that it takes days of experimentation and

YIFY/YTS Proxy List (May 2026): Working YIFY Mirror Sites

Updated YIFY/YTS proxy list with working links, safe access tips, and top alternatives for movies, including VPN and proxy methods.

How To Set Your Time And Zone Manually Windows

Some applications may not work properly with manual settings. Top 7 Ways To Set Your Time and Zone Manually On Windows 11/10 Here are seven

How to enable new Outlook app for Windows 11?

However, Microsoft plans to eventually phase out classic Outlook, so familiarizing yourself with the new interface is recommended for long-term

Why Your STM32F103VCT6 UART Interface Is Not Working

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Why Your STM32F103VCT6 UART Interface Is Not Working The STM32F103VCT6 is a popular microcontroller often used in embedded systems. However, when using its UART (Universal

Why is my audio interface not working: Troubleshooting Tips to Get

Adjusting audio settings is an essential troubleshooting step when your audio interface is not working properly. It is crucial to ensure that the input and output settings on your device are

STM32F103RET6, ST-LINK V2,

STM32F103RET6, ST-LINK V2, DEV_TARGET_CMD_ERR, SWD does not work properly Asked 3 years, 2 months ago Modified 1 year ago

SWD interface not working on STM32G0B1

So I don''t see how I could trigger the reset with better timing than the ST-Link doing it software-controlled :D I''ve never had any problems with the flash/debugging interface on other

STM32 & ST-LINK

Interestingly, both types of behaviour do not depend on the ST-LINK CN3 jumper positions on the STM32 board. (Why not?) Why can''t I use the SWD connector? Why does the ST-LINK/V2 do

Related Video Reference

This video was associated with the source search result. Verify technical details against current product documentation and project requirements.

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This document is for preliminary research. Verify specifications against current standards, product documentation and project requirements.

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